
INSTRUCTOR: Dr. Dimitri Kagaris, EGRE–111, tel. 453-7973, e-m: kagaris@engr.siu.edu
Office hours: Tue.Wed.Thu. 11:00–1:00

TEACHING ASSISTANT: Brett Patrick Delaney, bdelaney@siu.edu

Main Textbook:

<table>
<thead>
<tr>
<th>Evaluation</th>
<th>Percentage</th>
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<tbody>
<tr>
<td>Homeworks/Quizes</td>
<td>15%</td>
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<tr>
<td>Test I</td>
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<td>Test II</td>
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<td>Test III</td>
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<tr>
<td>Projects</td>
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COURSE OUTLINE

RISC Architectures.
Pipelining.
Pipeline Hazards.
Instruction Level Parallelism.
Memory Hierarchy Design, caches, virtual memory.
Vector Processors.
Multiprocessors.
• Grading Policy: A: 85-100; B: 75-84; C: 65-74; D: 50-65; F < 50.

• Classroom Policies:

1. Attendance Policy: Students are responsible for all announcements made in class.

2. Late Homework/Missed Exams: Late homework is not accepted. If an exam is missed for a provably legitimate reason, the student can take the exam at a designated time.

3. Mobile Technology Policy: Students must avoid use of electronic devices in the classroom during lecture time. It is forbidden to use electronics devices during exams or quizzes.