Most of the material of the class will be posted on this web page:
http://www.engr.siu.edu/staff1/botrosn/mywebpage/EE528.html

Instructor: Nazeih M. Botros, Professor, Electrical and Computer Engineering, E117, e-mail botrosn@siu.edu, Phone: 618-453-7028.

Office Hours: MWF 10:00-12:00

Teaching Assistants: TBA

Prerequisites by Topic:
Computer Architecture


References:
IEEE-Transactions on Circuits and Systems, Computers, CAD, and other Compatible Transactions and Journals.

Xilinx manuals


Grading:
Labs and assignments 8%
Presentations 23%
Exam I 20%
Some of the above 20 points are given as pop quizzes or homework.

Exam II 20%
Some of the above 20 points are given as pop quizzes or homework.

Exam III (see final exam schedule) 20%
Some of the above 20 points are given as pop quizzes or homework.
A. The lecture part of the course closely follow the textbook.

- Computing and Computers
- Design Methodology
- Processor basics
- Data-path Design
- Control design
- Memory Organization
- System Organization

B. Presentation by the students

Each group of 5 students presents a paper from IEEE-Transactions 1995 or later. The paper should address one of the topics of the course. Papers from Transactions on Circuits and Systems, Computers, CAD, or other compatible IEEE-Transactions are acceptable. The paper has to be approved by me before presentation. Each group has a 30 minutes presentation time divided equally between the members of the group. Use power point presentation. Prepare a one or two-page summary of the paper to be distributed to the class.