### Instructor:
Nazeih M. Botros, Professor, Electrical and Computer Engineering, E117, e-mail botrosn@siu.edu, Phone: 618-453-7028.

### Office Hours:
MTW 10:00-12:00

### Teaching Assistants:
TBA

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<tr>
<th>T.A. Lab/Office Hours</th>
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### Goals:
This course is designed to give seniors in electrical engineering an ability to design the state of art of digital systems.

### Prerequisites by Topic:
1. Knowledge of computer programming
2. Number systems
3. Boolean Algebra
4. Combinational Circuits
5. Sequential Circuits

### Textbooks:
1. Computer Organization and Design, 4th edition, by David A. Patterson and John L. Hennessy. Morgan Kaufmann Publishers. If you have older edition, please consult with me to see if you can use it
3. Class notes

### References:

### Grading:

<table>
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<tr>
<th>Component</th>
<th>Percentage</th>
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<tbody>
<tr>
<td>Homework</td>
<td>5%</td>
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<tr>
<td>Labs</td>
<td>11%</td>
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<tr>
<td>Project</td>
<td>12%</td>
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<tr>
<td>First exam</td>
<td>24%</td>
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<tr>
<td>Second exam</td>
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The lab hours will be determined at the beginning of the semester. We will focus on lab experiments this semester more than what we did in the last semesters. The lab score will include lab quizzes and tests. The lab and the lecture time may be swapped once or twice during the semester. Details will be given in the first class of the semester.
Some points of the above exam will be earned as pop quizzes in class.

Final exam, non-comprehensive, See University Final Exam Schedule: 24%

Some points of the above exam will be earned as pop quizzes in class.

If any exam is missed for a legitimate reason, a make-up will be given.

A 90-100%, B: 80-89, C: 70-79, D: 60-69, F: <60