PhD Dissertations – all students received support from his research grants

6. Adam Watkins, Analysis and Mitigation of Multiple Radiation Induced Errors in Modern Circuits, ECE Dept., SIUC, November 2016. Currently with Collins Aerospace, Cedar Rapids, IA.
14. Kedar Karmakar, Scalable bus encoding for error-resilient high-speed on-chip communication, ECE Dept., SIUC, June 2013. Currently with Intel Corporation, Hillsboro, OR.
15. Sreenivas Gangadhar, Analytical Methods to Propagate and Diagnose Single Event Transients, ECE Dept., SIUC, August 2012. Currently with Intel Corporation, Austin, TX.
17. Manoj Kumar Goparaju, Coping with discrepancies of the manufactured weights in Threshold Logic gates, ECE Dept., SIUC, December 2009. Currently with Qualcomm, Raleigh, NC.
23. Khadija.J. Stewart, Emerging technologies Involving Networks, ECE Dept., SIUC, August 2006. Currently tenured Assoc. Professor, Computer Science Dept., Depauw University, Newcastle, IN.
25. Saravanan Padmanaban, Non-enumerative techniques for delay verification and diagnosis, College of Engineering, SIUC, August 2003. Currently with Intel Corporation, Hillsboro, OR. (Previously with the ECE Dept., Univ. of Maryland Baltimore County.)
26. Maria Michael, Test-based timing verification using functional techniques, College of Engineering, SIUC, 2002. Currently tenured Assoc. Professor, ECE Dept., Univ. of Cyprus. (Previously Assist. Professor, CSE Dept., Univ. of Notre Dame.)


28. Cheng Luo (Primary advisor W.-C. Hu, Computer Science Department), SIUC, 2007. Currently with Computer Science Department, Coppin State University, Baltimore, MD


30. Dimitri Kagaris (Primary advisor F. Makedon), Computer Science Department, Dartmouth College, 1994. Currently with Electrical and Computer Engineering Department, SIUC.

Recent MS Theses (since 2014) – all students received support from his grants
- Aashish Itani, Comparison of Adversarial Robustness of ANN and SNN towards Black Box Attacks, May 2021. Currently with Intel, Santa Clara, CA.
- Chandra Sagili, Multi-threading accelerator for SMT based RTL verification, ECE Dept. SIUC, October 2019. Currently with Cadence, CA.
- Damianos Veskoukis, Automatic MC/DC Test Pattern Generation, June 2018. Currently with Oxford University, UK (PhD program)
- Fatemeh Mohammadi Shakiba, CMOS-based implementation of hyperbolic tangent activation function for artificial neural network, March 2018. Currently with NJIT (PhD program).
- Ajitkumar Yadav, Fault Diagnosis in Failed Functional Sequences at RTL Level, ECE Dept., August 2016. Currently with AMD, Orlando, FL.
- Ehsan Ahmadi, Solving Incremental Specifications using Z3 SMT Solver, ECE Dept., SIUC, (August 2016). Currently with the Univ. of Wisconsin (PhD program).
- Theodore Toulas, Transition fault-driven delay defect diagnosis in the presence of process variations, ECE Dept., SIUC, December 2015. Currently with Synopsys, Mountain View, CA.
- Vasiliki Gkintzou, Preemptive edge-based scheduling for real-time networks on-a-chip, ECE Dept., SIUC, October 2014. Currently with Advantest, San Jose, CA.